

**Form PTO-1449 (modified)** *JCT*  
List of Patents and Publications  
For Applicant's Information  
Disclosure Statement  
*(Use several sheets if necessary)*

ATTY. DKT. NO. 5500-97300      SERIAL NO. 10/655,389

APPLICANT: Michael G. Butler      GROUP: 2186

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## **U.S. PATENT DOCUMENTS**

## **FOREIGN PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATIO YES/NO

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

22	A6	Kobayashi, Ryotaro et al., "A Cost-Effective Branch Target Buffer with a Two-Level Table Organization", Proceedings of the Second International Symposium of Low-Power and High-Speed Chips (COOL Chips II), p. 267, April 1999.

**EXAMINER:**

✓ ✓

**DATE CONSIDERED:**

10/11/05

**EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.